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CONTROLLING DUAL PROCESSORS IN CELLULAR TELEPHONES

Abstract of the Disclosure

A cellular telephone using a pair of processors may include a bypass device. The bypass device may be active when a first processor is not responding. In such case, inputs from a keypad or outputs to the display may bypass the first processor allowing continued communication between input/output devices and the second processor. In some embodiments, the first processor may be an applications processor and the second processor may be a baseband processor.